

FIG. 1

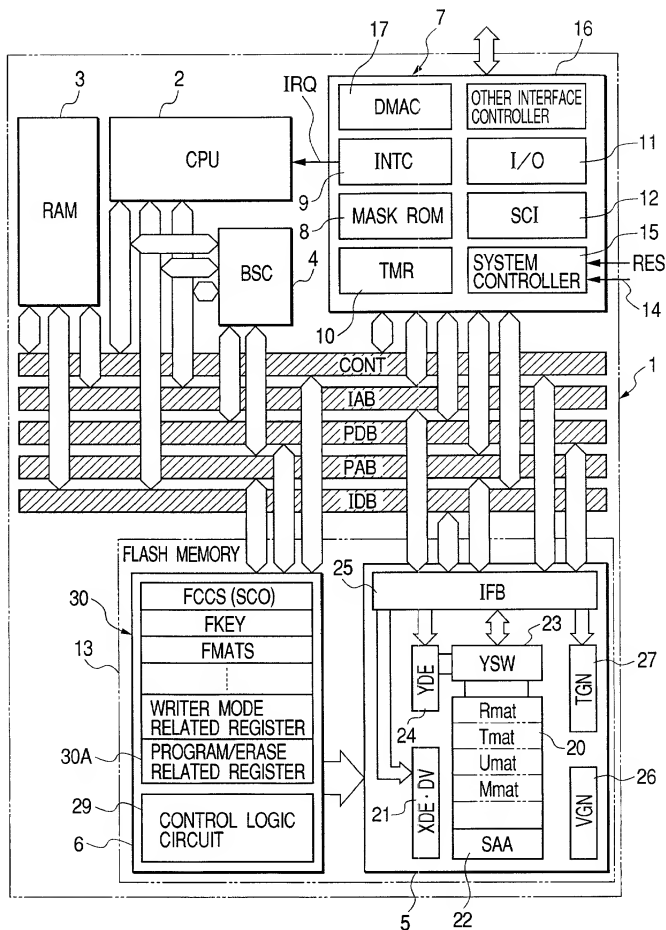


FIG. 2

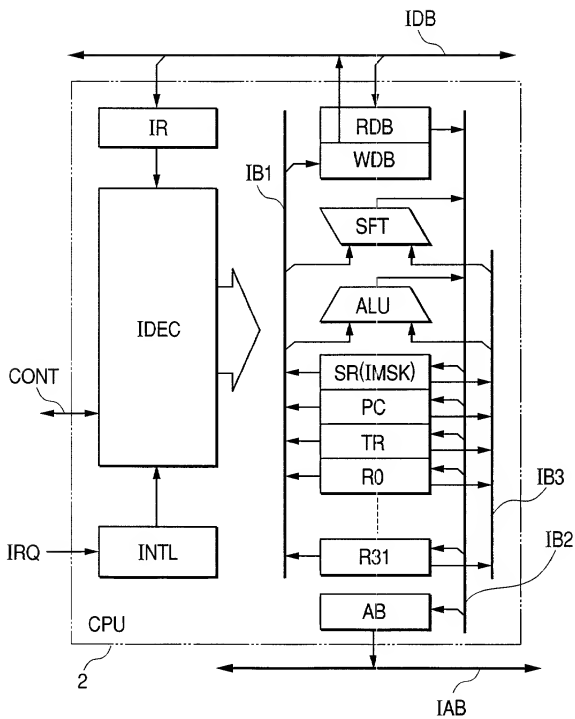


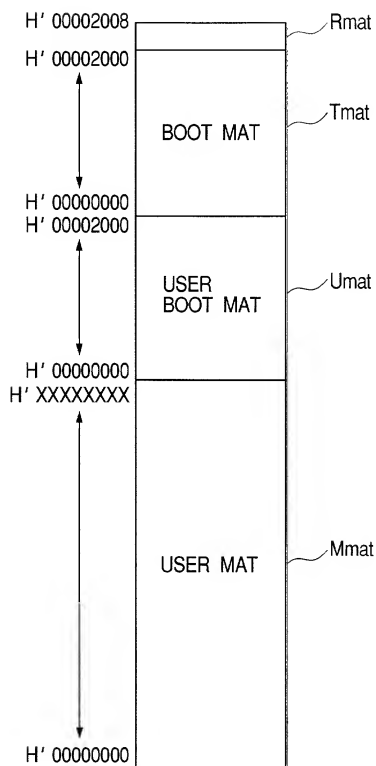
FIG. 3

FIG. 4(A)

OPERATION MODE MAT	BOOT MODE			WRITER MODE		
	ACCESS	PROGRAM	ERASE	ACCESS	PROGRAM	ERASE
USER MAT (Mmat)	○	○	○	○	○	○
USER BOOT MAT (Umat)	○	○	○	○	○	○
BOOT MAT (Tmat)	△	×	×	△	×	×
REPAIR & TRIMMING (Rmat)	×	×	×	×	×	×

FIG. 4(B)

OPERATION MODE MAT	USER BOOT MODE			USER MODE		
	ACCESS	PROGRAM	ERASE	ACCESS	PROGRAM	ERASE
USER MAT (Mmat)	○	○	○	○	○	○
USER BOOT MAT (Umat)	○	×	×	○	×	×
BOOT MAT (Tmat)	△	×	×	△	×	×
REPAIR & TRIMMING (Rmat)	×	×	×	×	×	×

FIG. 5

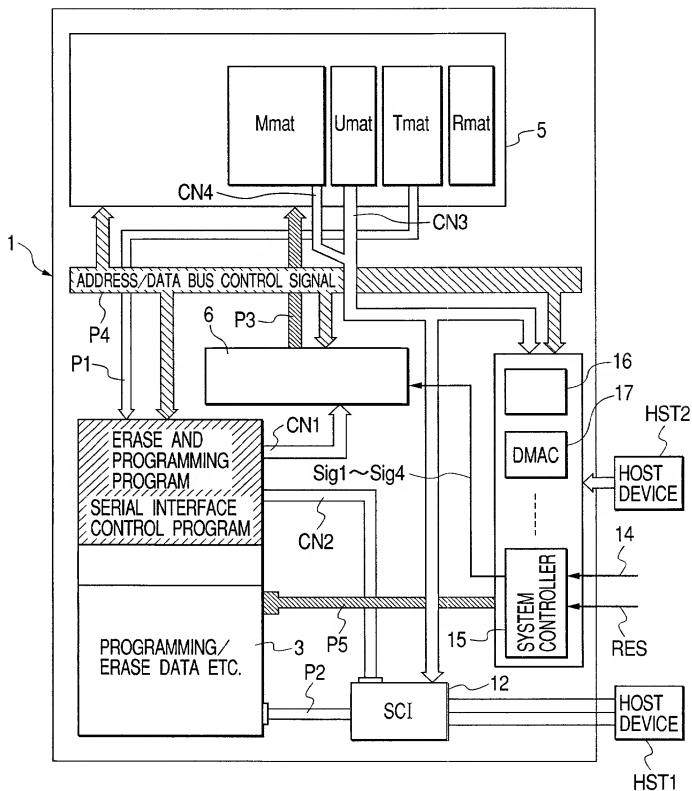


FIG. 6

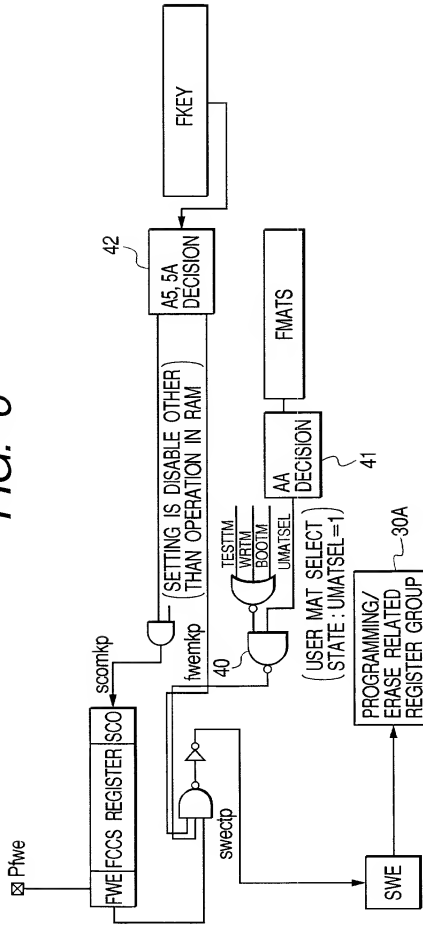


FIG. 7

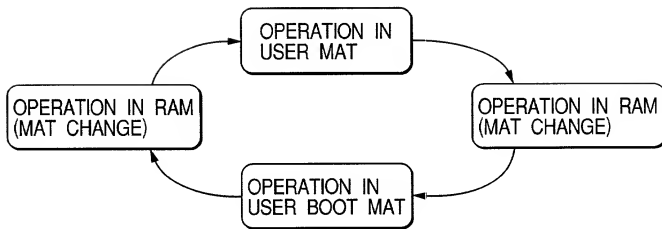


FIG. 8

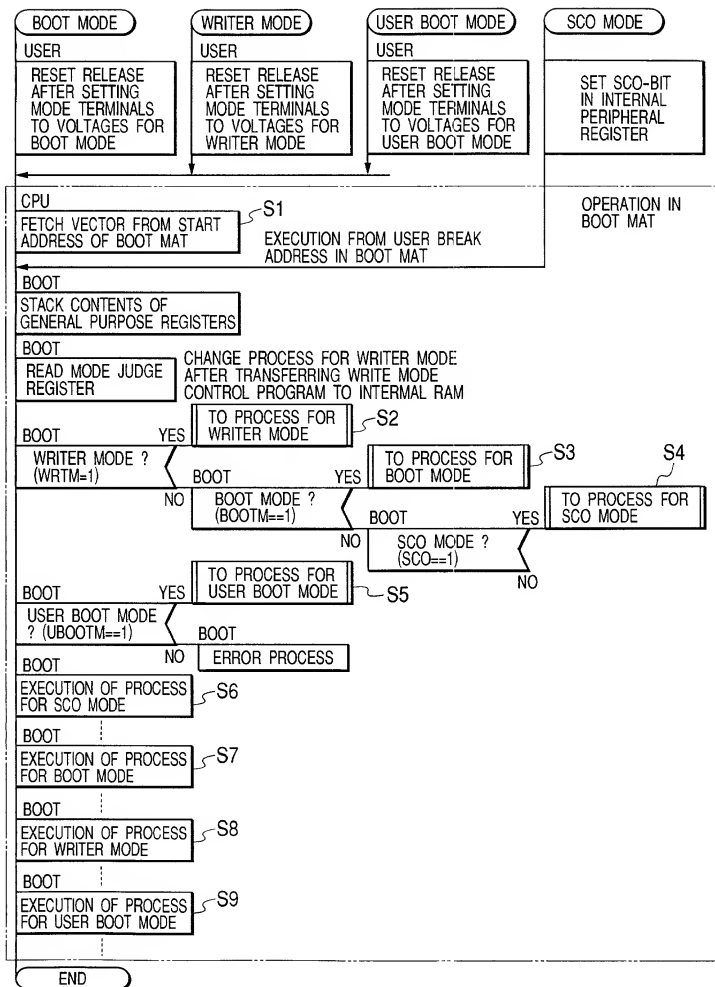


FIG. 9

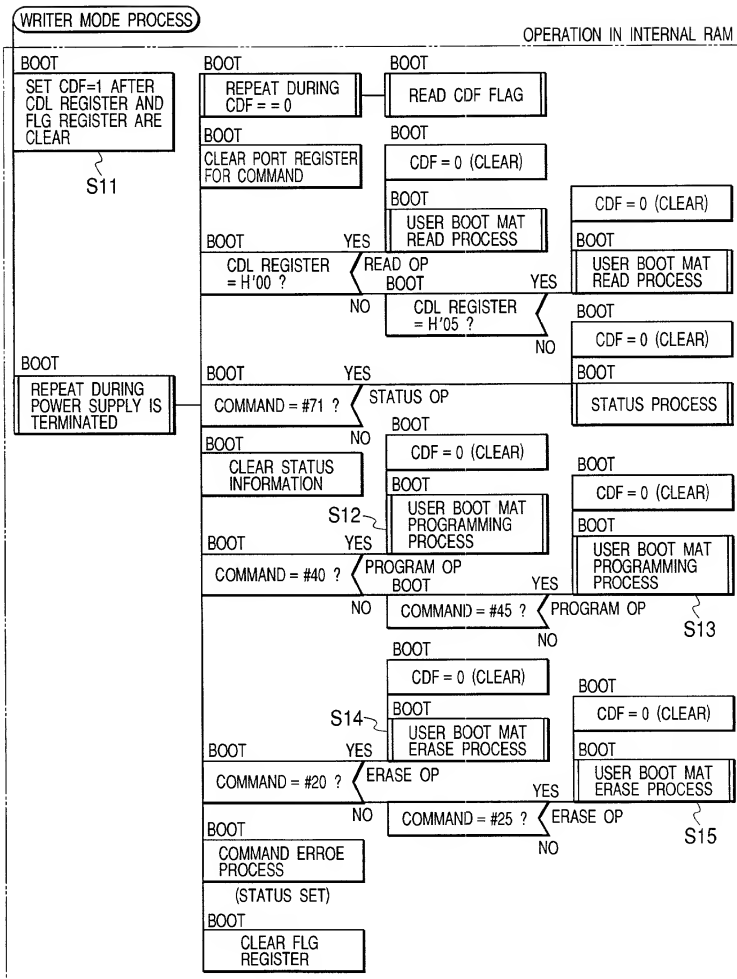


FIG. 10

BOOT MODE PROCESS

USER, BOOT

ESTABLISH TRANSMISSION
AND RECEPTION TO SCI

S20

USER, BOOT

BAUD RATE CHANGE, AND
PROGRAM/ERASE PARAMETER
CALCULATION

S21

USER, BOOT

DECODE COMMAND FROM HOST
AND RETURN TO HOST STATUS
ON PRODUCT NAME, NUMBER OF
ERASE BLOCKS AND MATS

S22

ESTABLISH SERIAL COMMUNICATION
AND DOWN LOAD REQUIRED
PARAMETERS FROM HOST

BOOT

TRANSFER COMMAND DECISION
PROGRAM AND ERASE PROGRAM
TO RAM, AND CHANGE TO
OPERATION IN INTERNAL RAM

S23

BOOT

YES

NORMAL END ALL
THE BLOCKS ARE
ERASED ?

NO

BOOT

TRANSMIT FF TO HOST

S24

USER

END PROCESS

USER

ISSUE BOOT COMMAND
USING SCI

BOOT

PROGRAM USER
MAT

S25

BOOT

YES

USER MAT PROGRAM
COMMAND ?

BOOT

PROGRAM USER
BOOT MAT

S26

BOOT

NO

YES

USER BOOT MAT
PROGRAM COMMAND ?

BOOT

PROGRAM USER
MAT DUAL BANK

S27

BOOT

YES

NO

DUAL BANK PROGRAM
COMMAND ?

BOOT

READ USER
BOOT MAT

S28

BOOT

NO

YES

USER MAT READ
COMMAND ?

BOOT

READ USER MAT

S28

BOOT

YES

NO

USER BOOT MAT
READ COMMAND ?

BOOT

EXECUTION OF
COMMAND ERROR
PROCESS

S28

USER

REPEAT UNTIL
PROCESSING OF
THE PURPOSE IS
COMPLETEDEXECUTION OF
PROGRAM/ERASE
OPERATION IN
INTERNAL RAM

END

FIG. 11

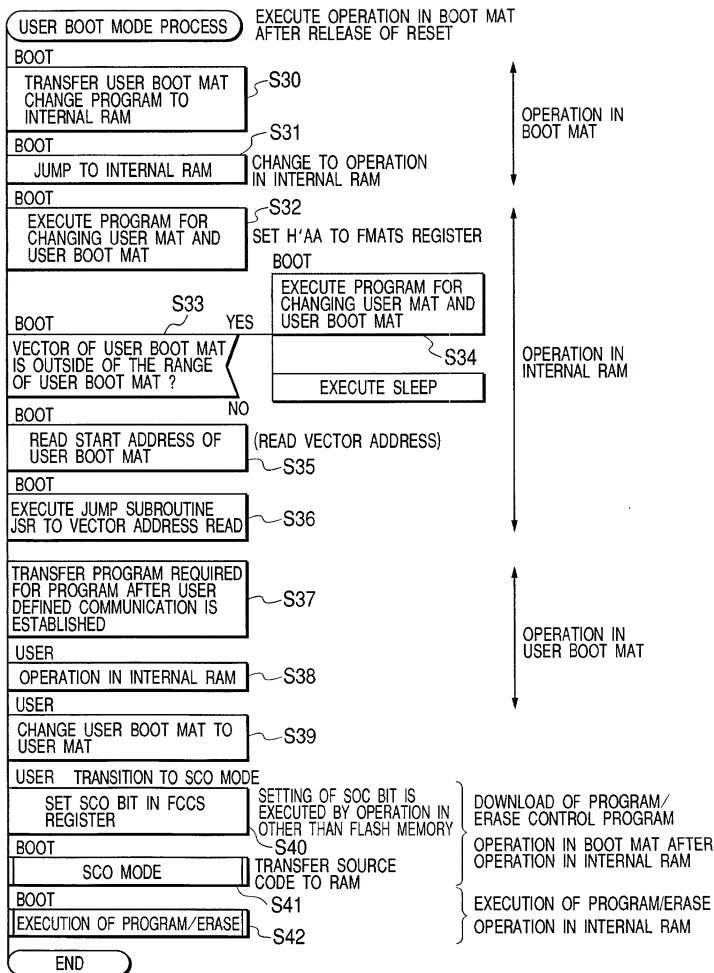


FIG. 12

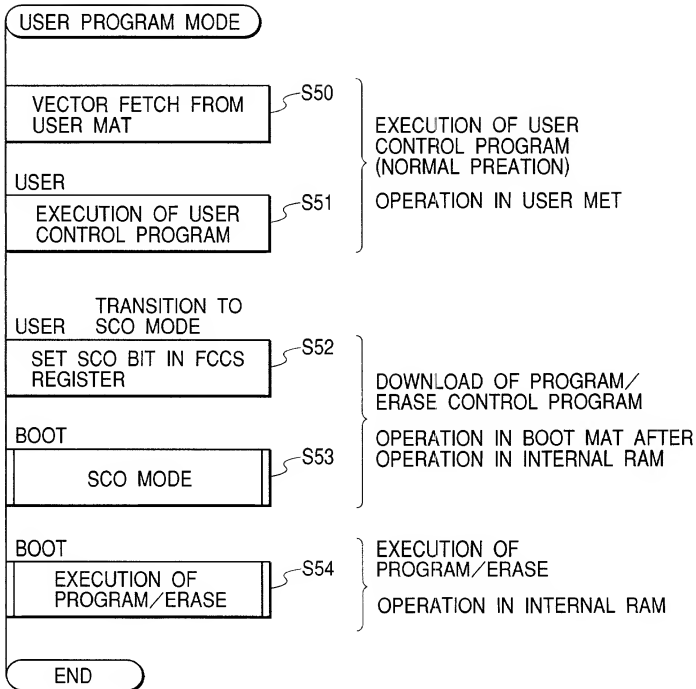


FIG. 13

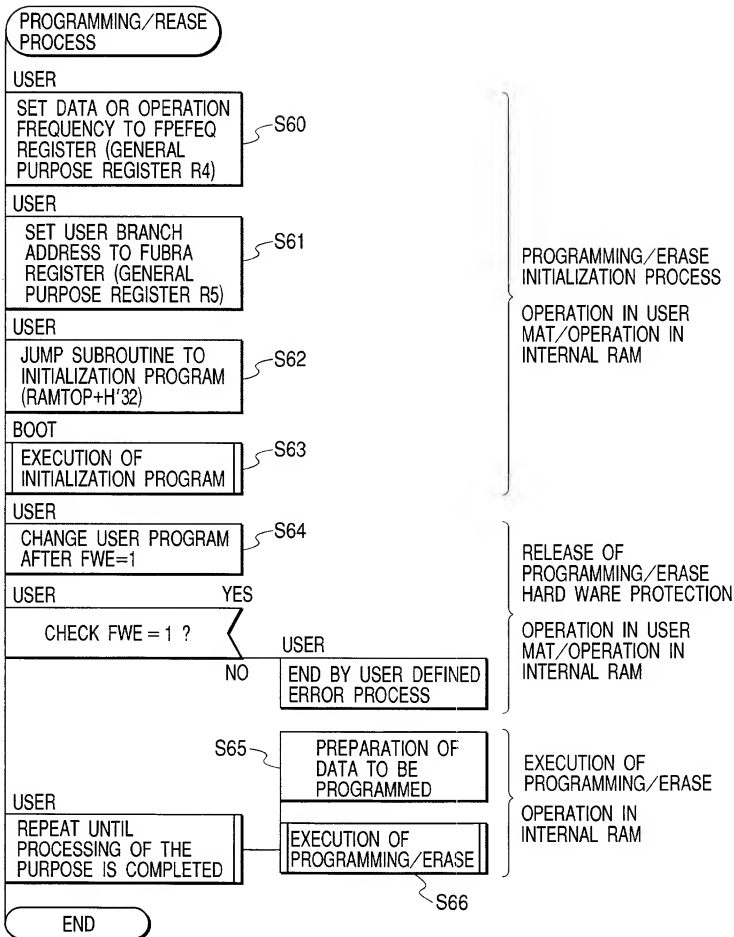


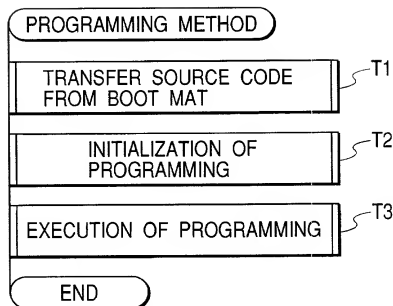
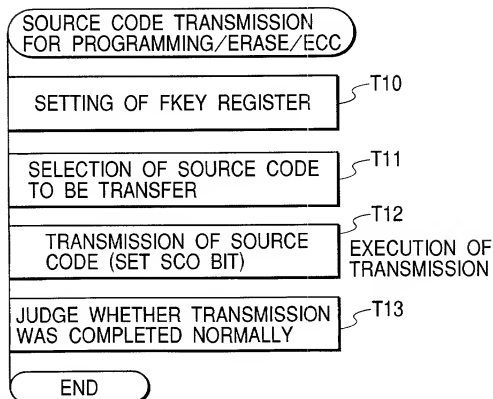
FIG. 14**FIG. 15**

FIG. 16

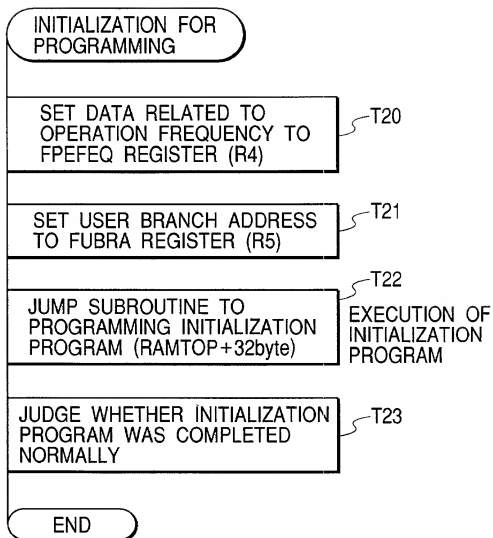


FIG. 17

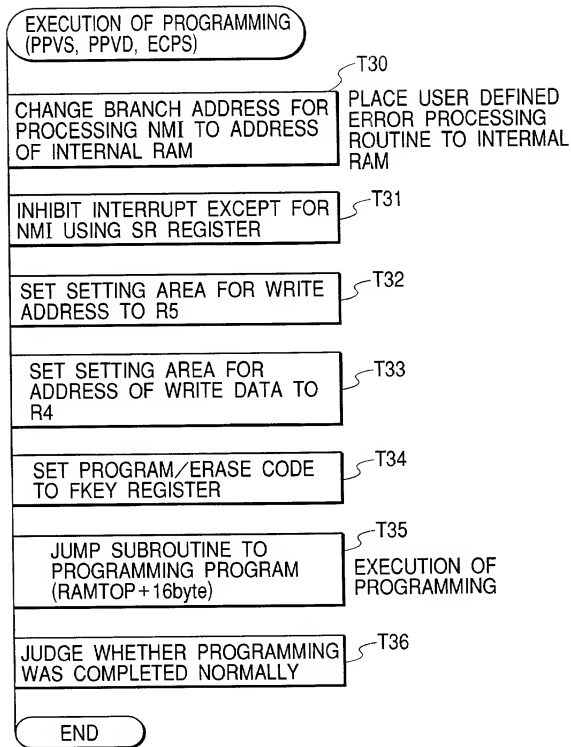


FIG. 18

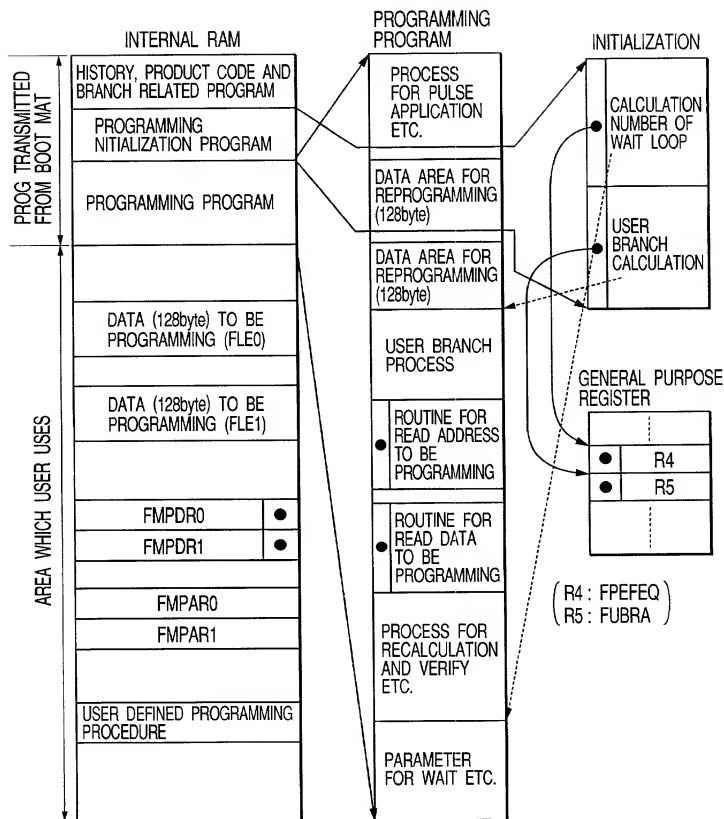


FIG. 19

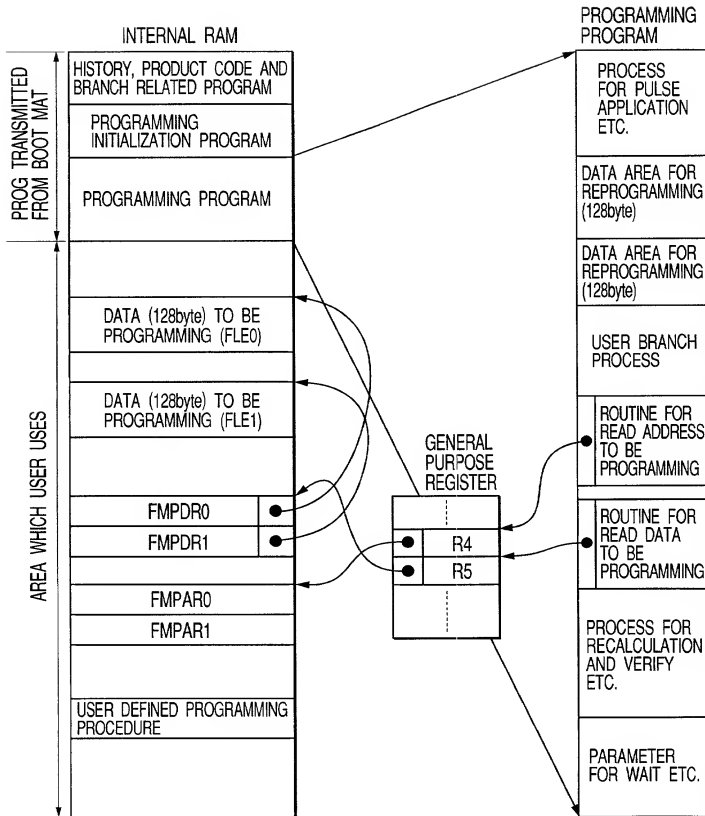


FIG. 20

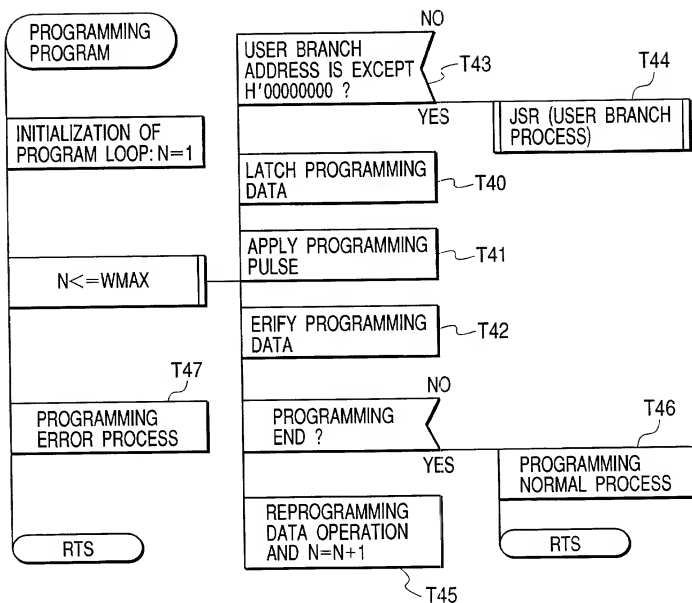


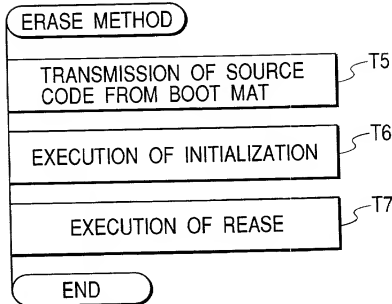
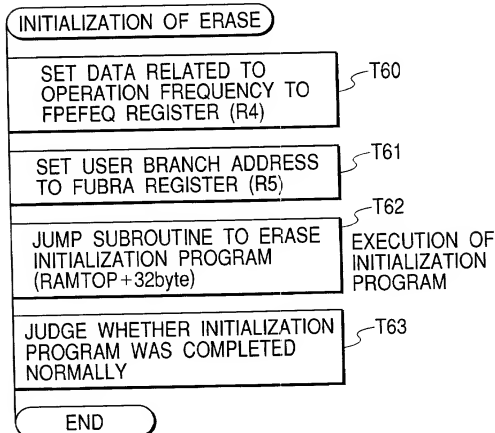
FIG. 21**FIG. 22**

FIG. 23

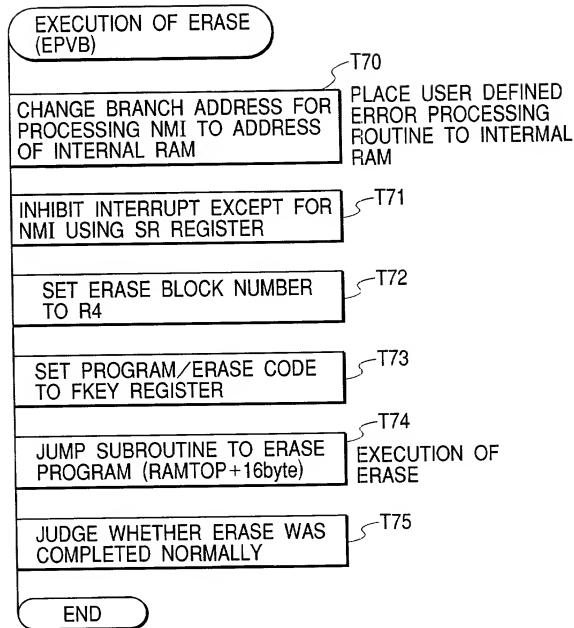


FIG. 24

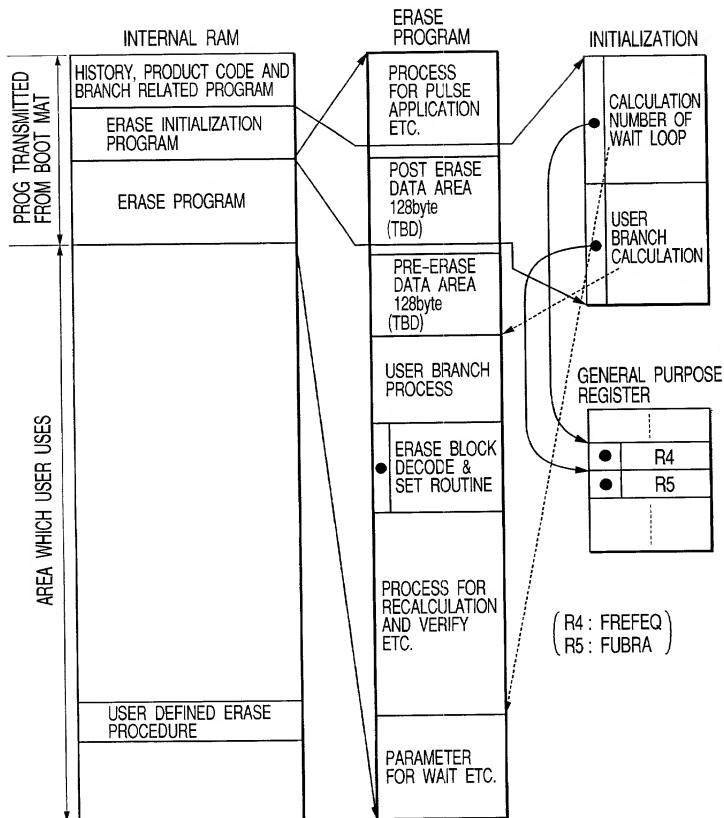


FIG. 25

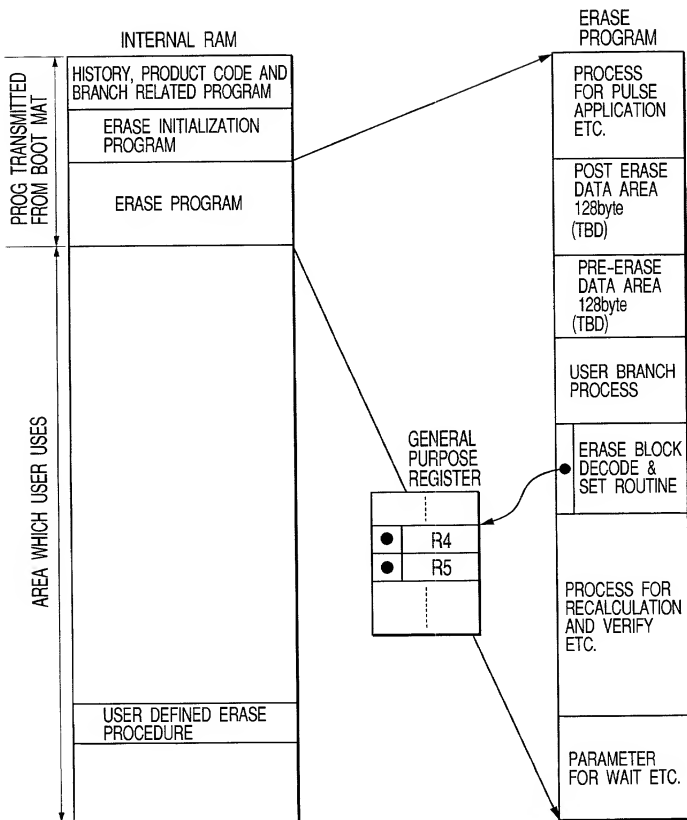


FIG. 26

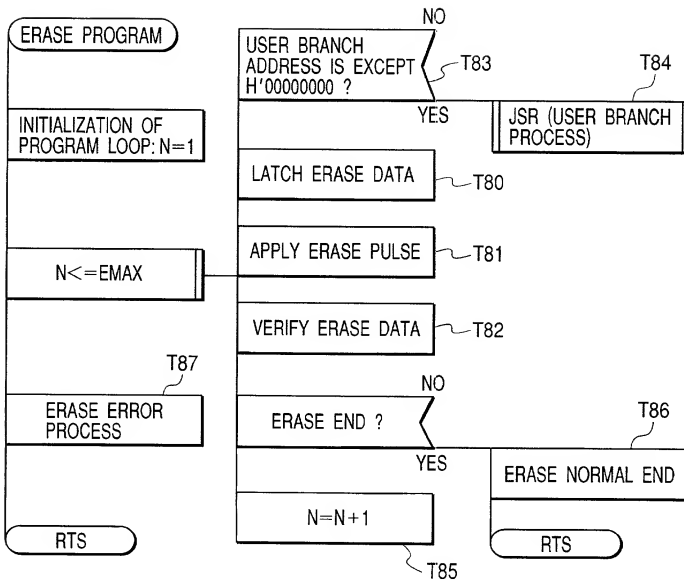


FIG. 27

